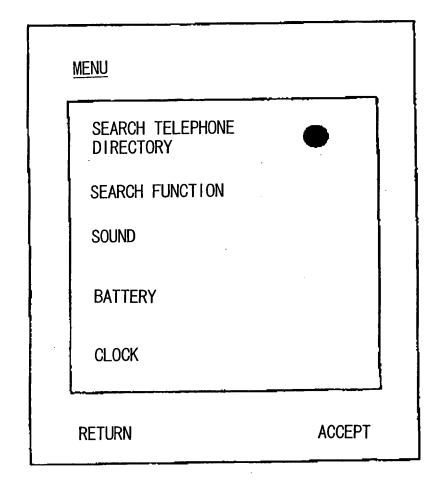
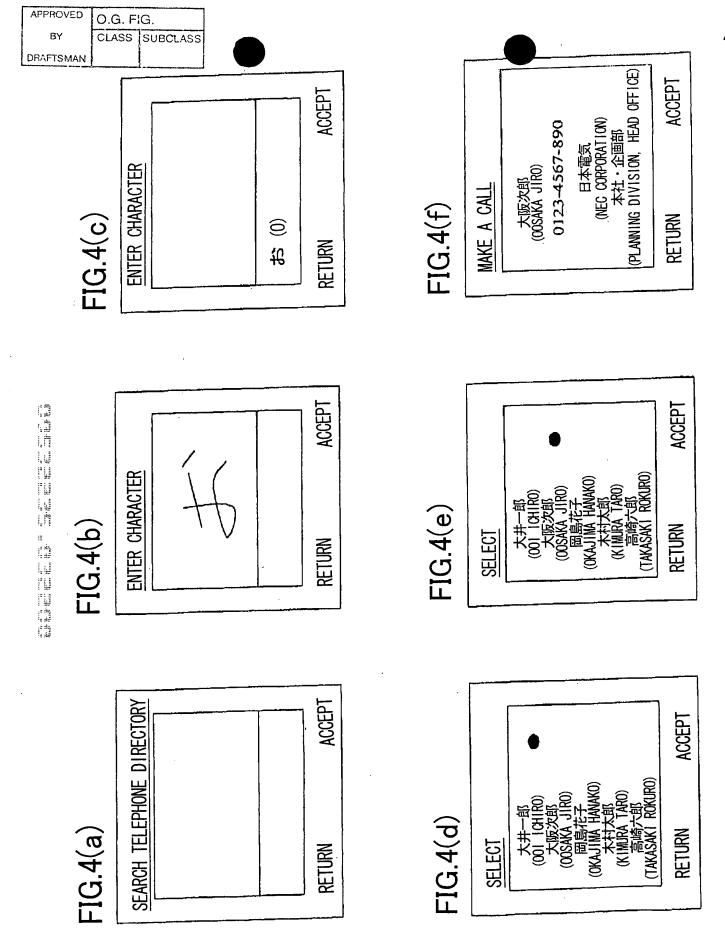


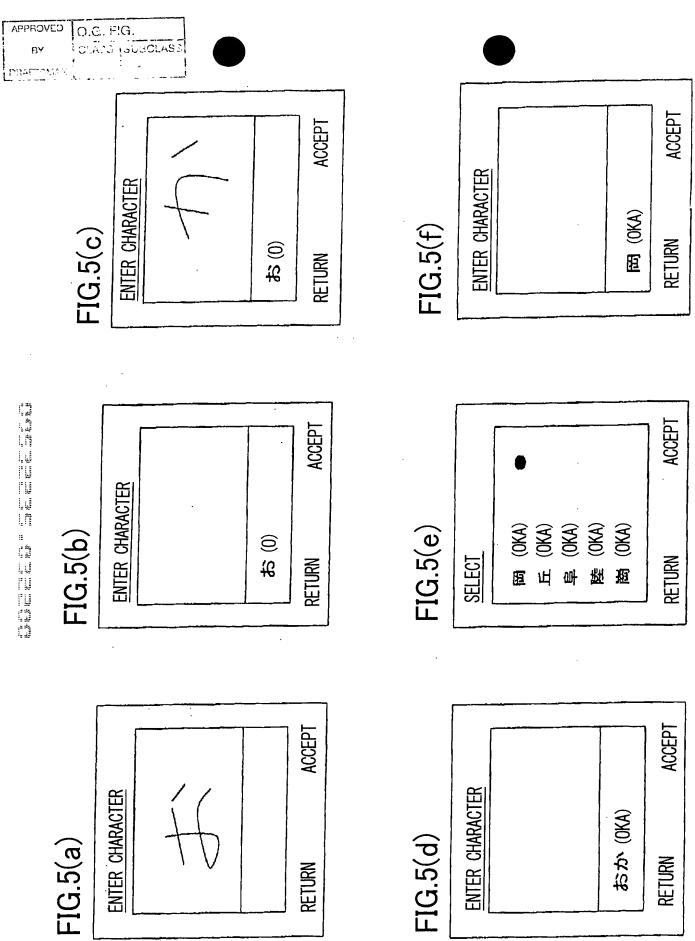
35 34 <del>(</del> TOUCH PANEL ENTRY PROCESSOR MAIN OPERATION PART TOUCH PANEL 36 33 CPU S

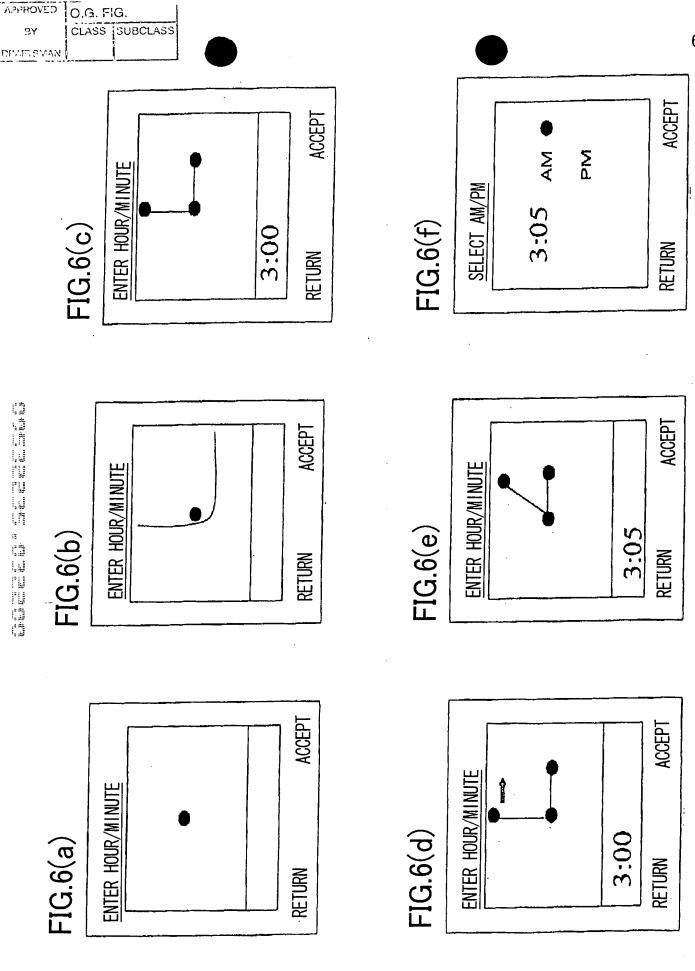
FIG . 3

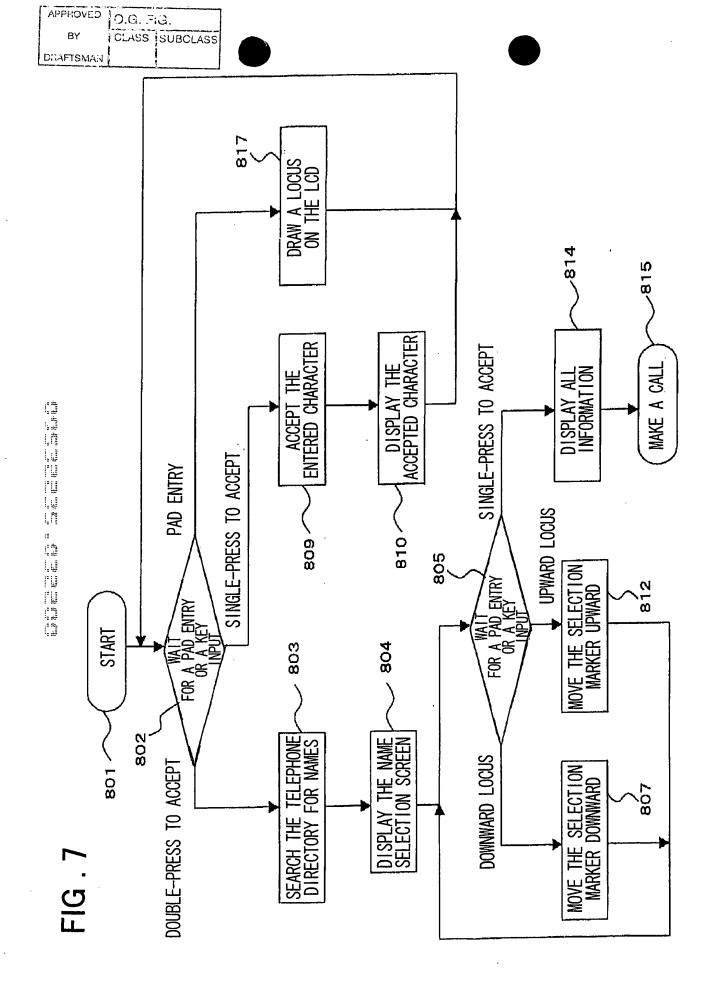


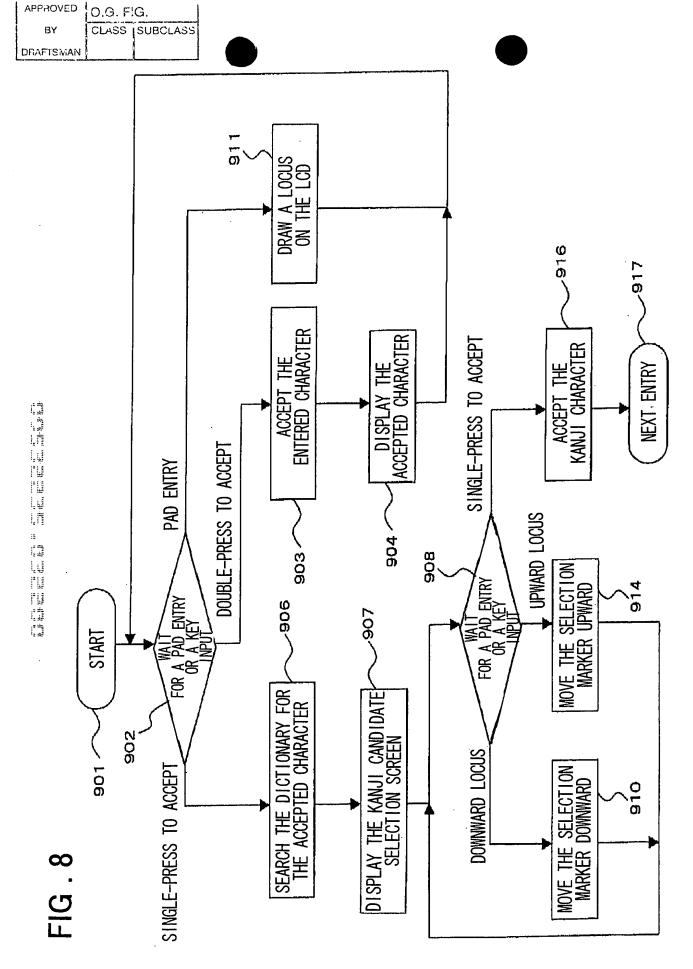
3. The state of th

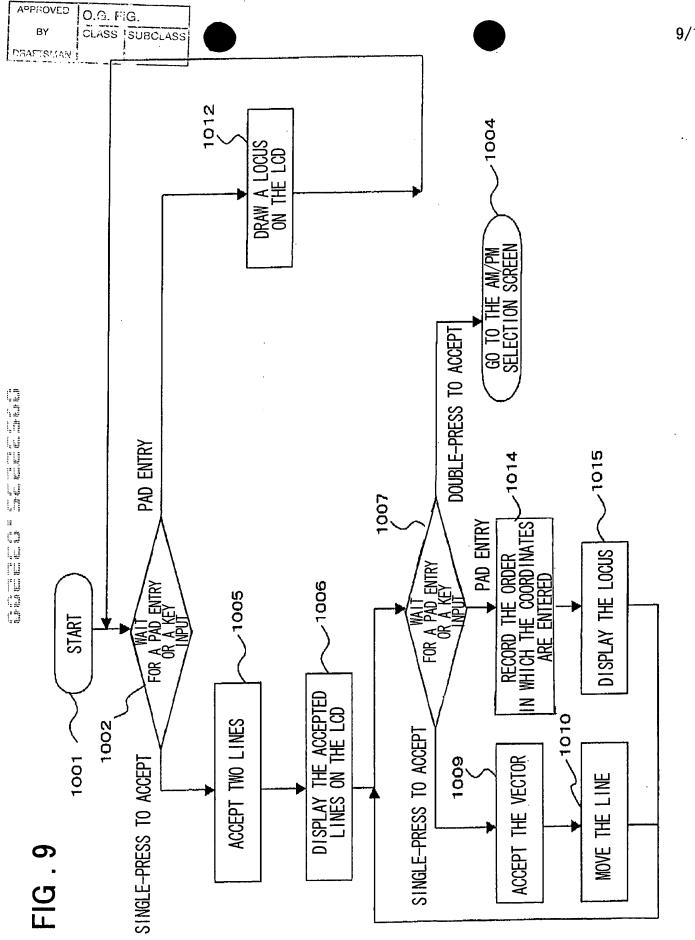


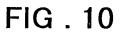












## PRIOR ART

